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FIG. 1

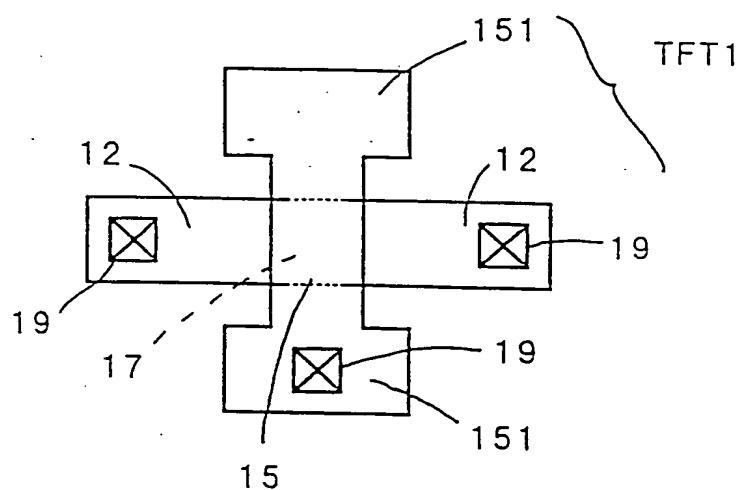
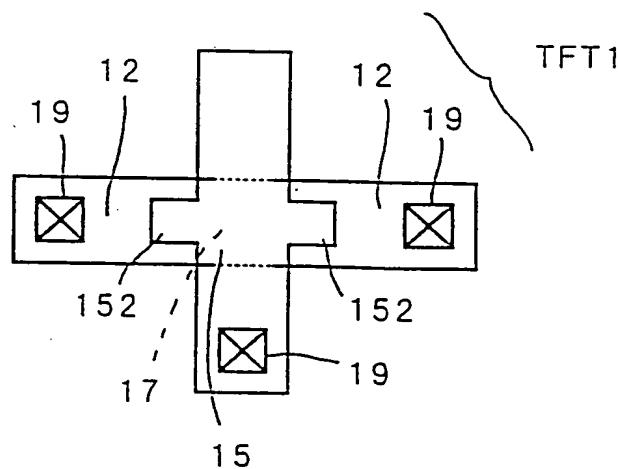


FIG. 2



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FIG. 3

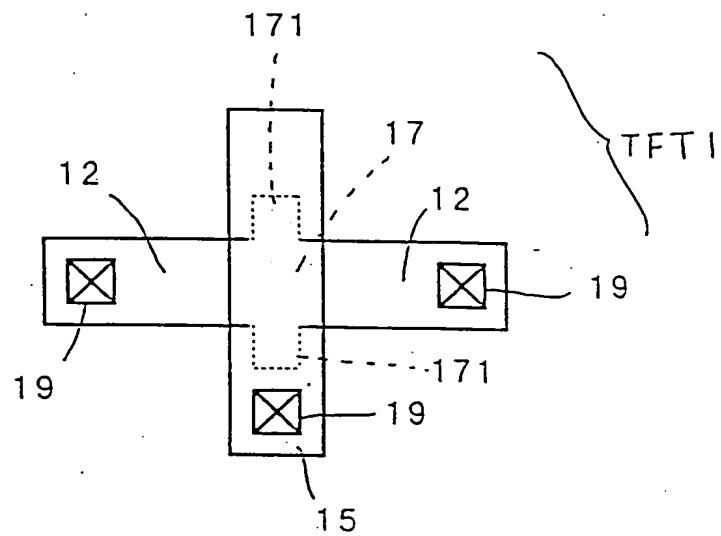
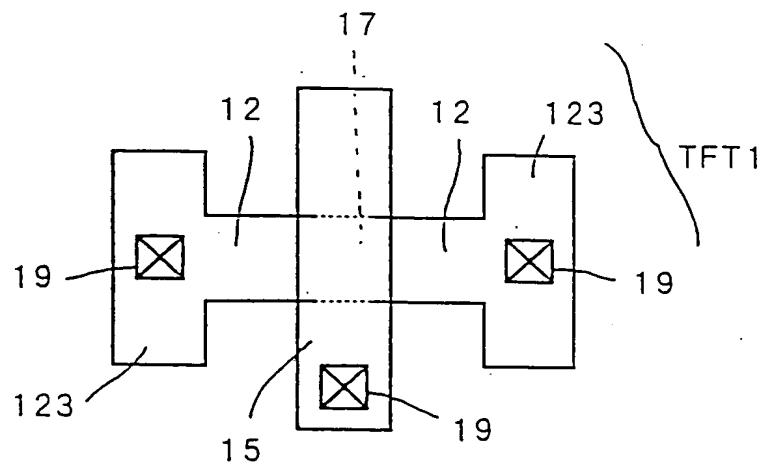
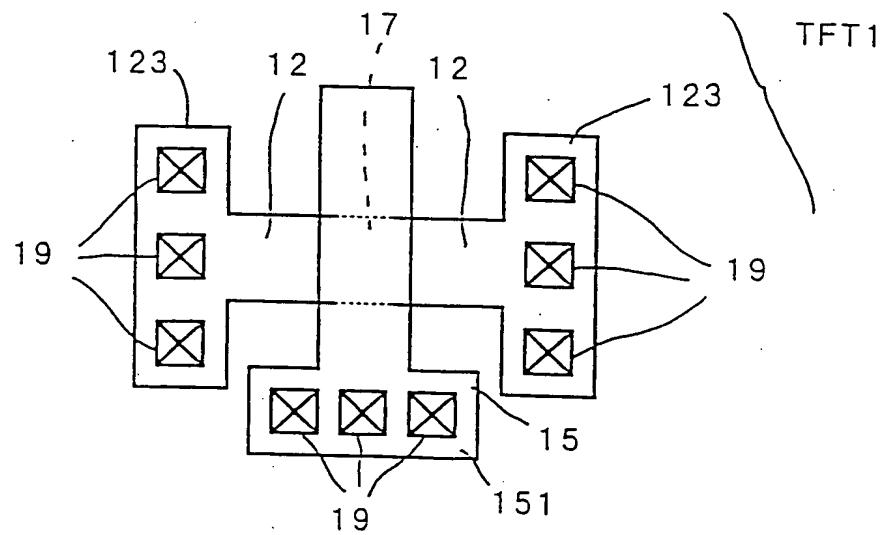


FIG. 4



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FIG. 5



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FIG. 6

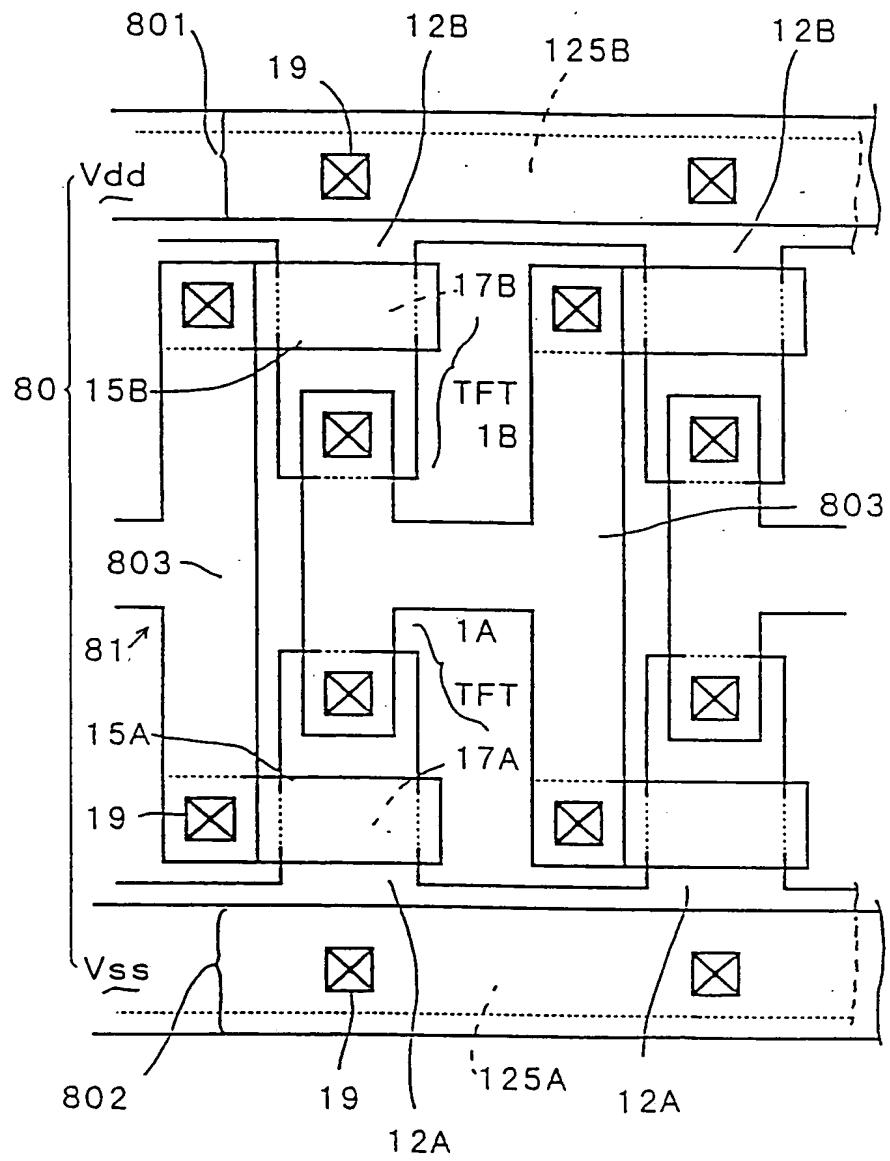
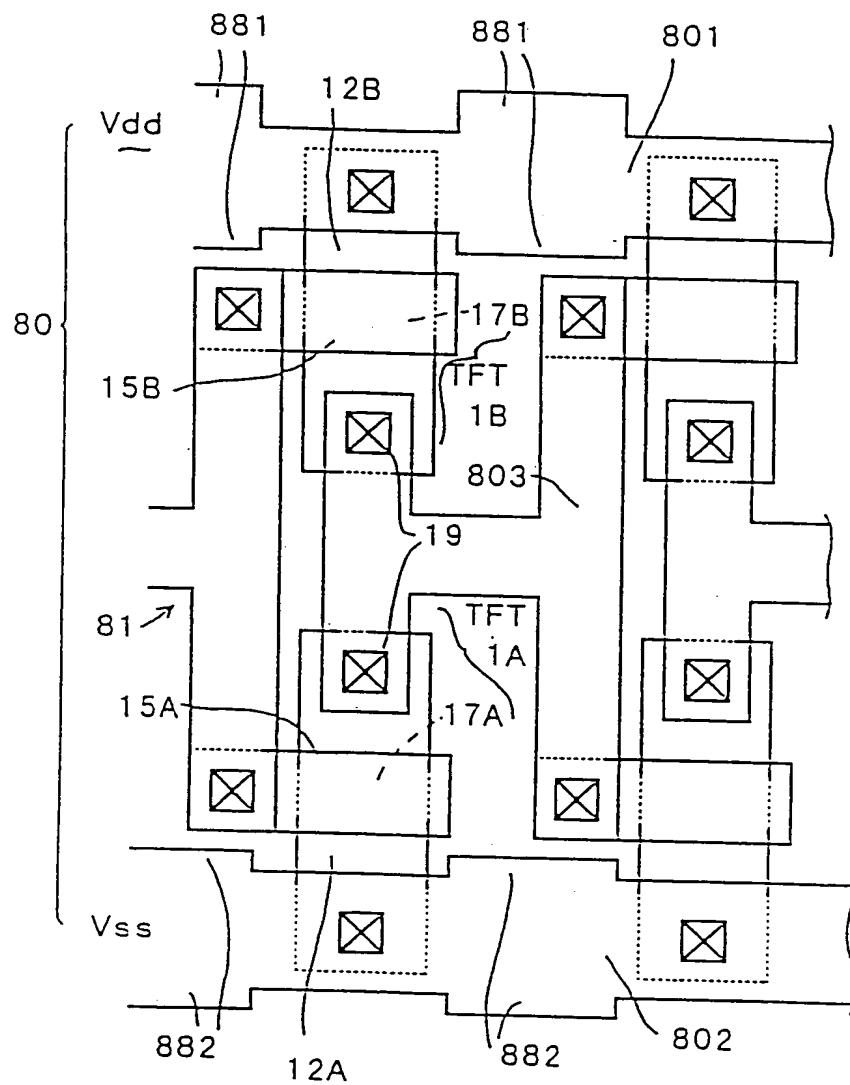
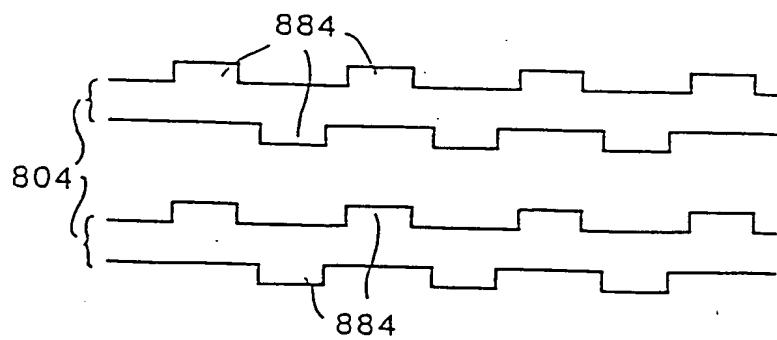


FIG. 7

(A)

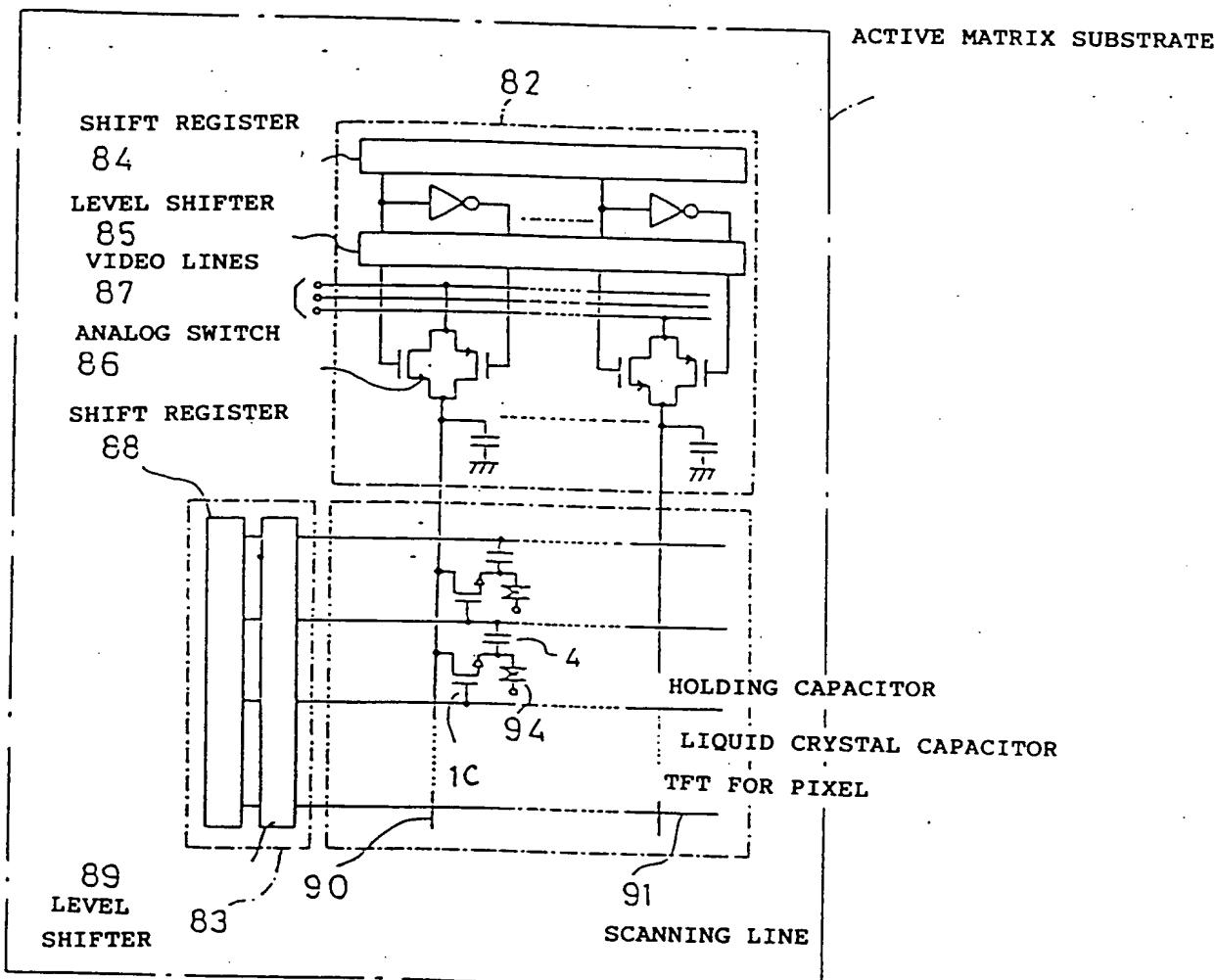


(B)



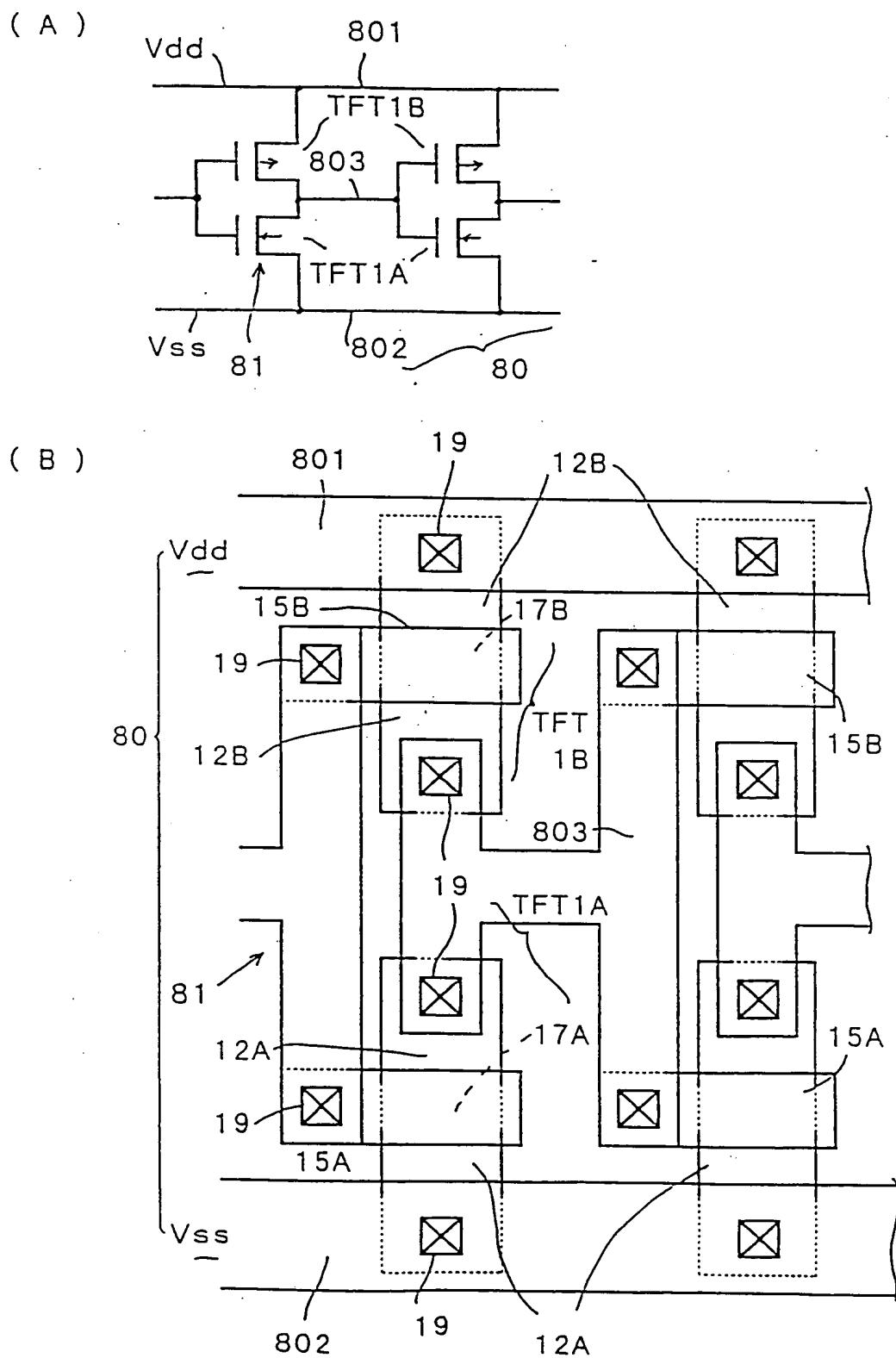
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FIG. 8



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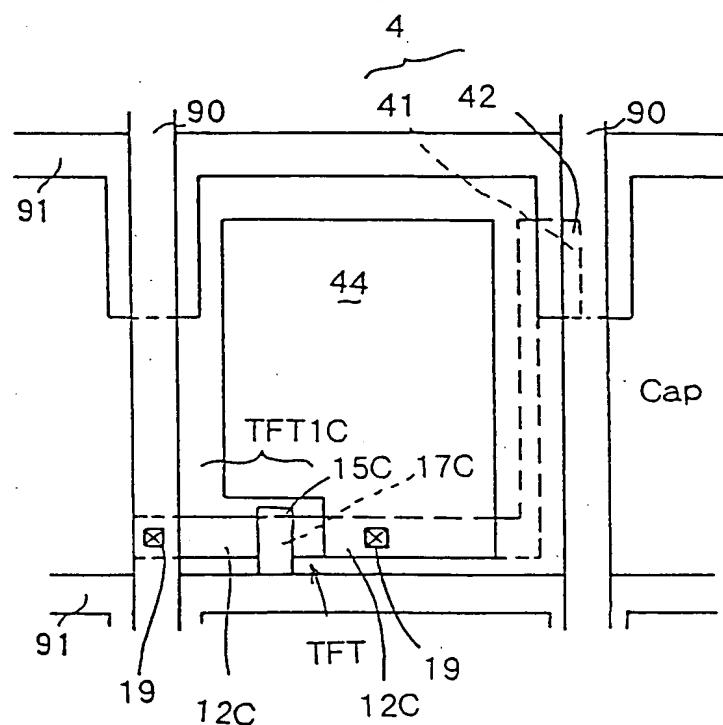
FIG. 9



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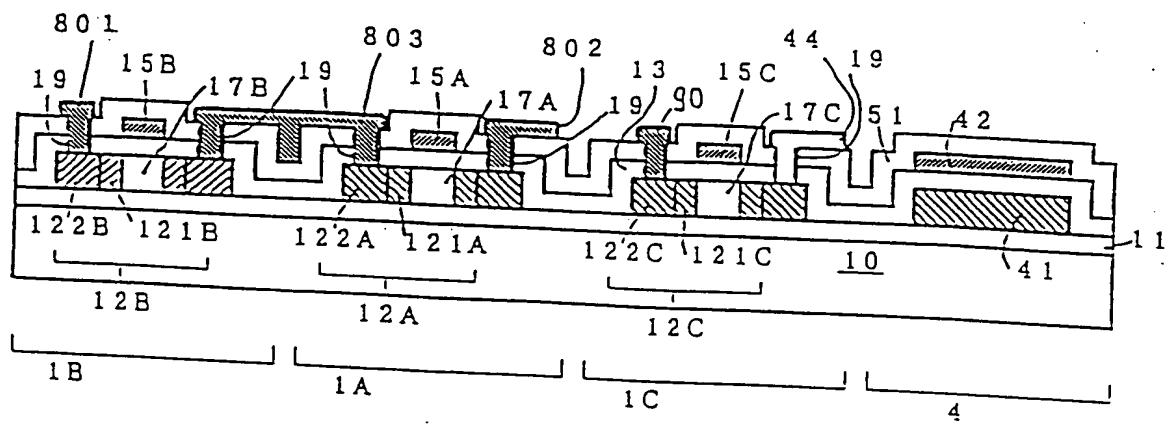
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FIG. 10



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FIG. 11



P-TYPE TFT FOR
DRIVING CIRCUIT

N-TYPE TFT FOR
DRIVING CIRCUIT

TFT FOR PIXEL

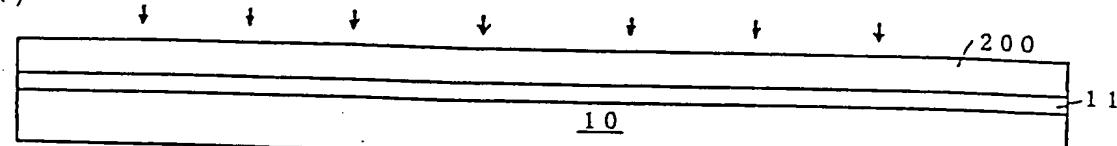
HOLDING CAPACITOR

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FIG. 12

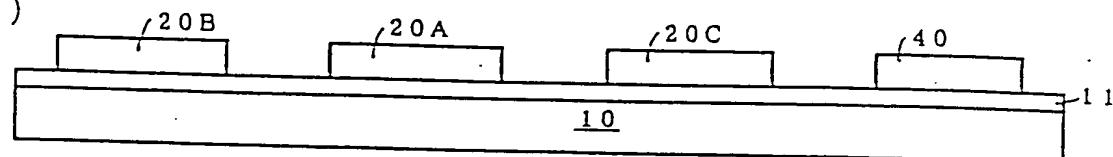
LASER ANNEALING

(A)

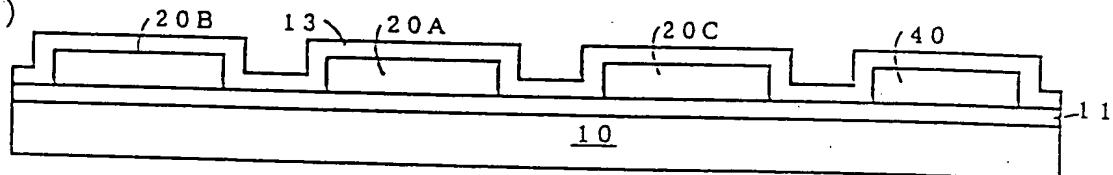


P-TYPE TFT FOR DRIVING CIRCUIT N-TYPE TFT FOR DRIVING CIRCUIT TFT FOR PIXEL HOLDING CAPACITOR

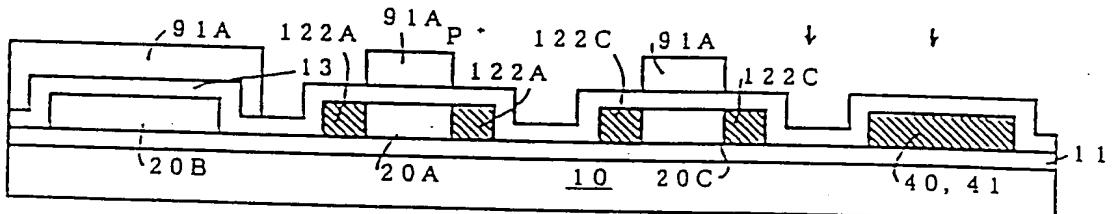
(B)



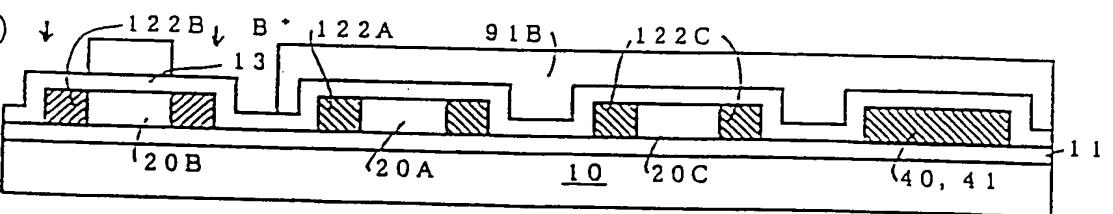
(C)



(D)



(E)



(F)

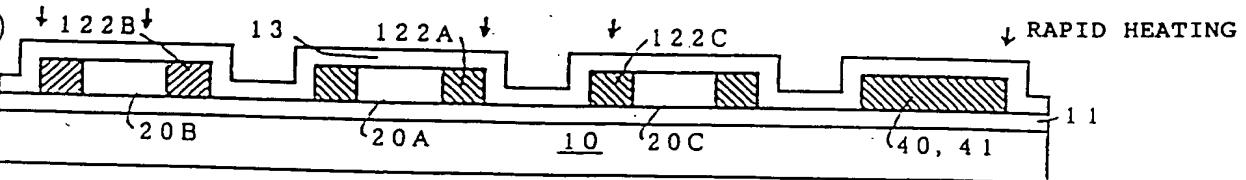
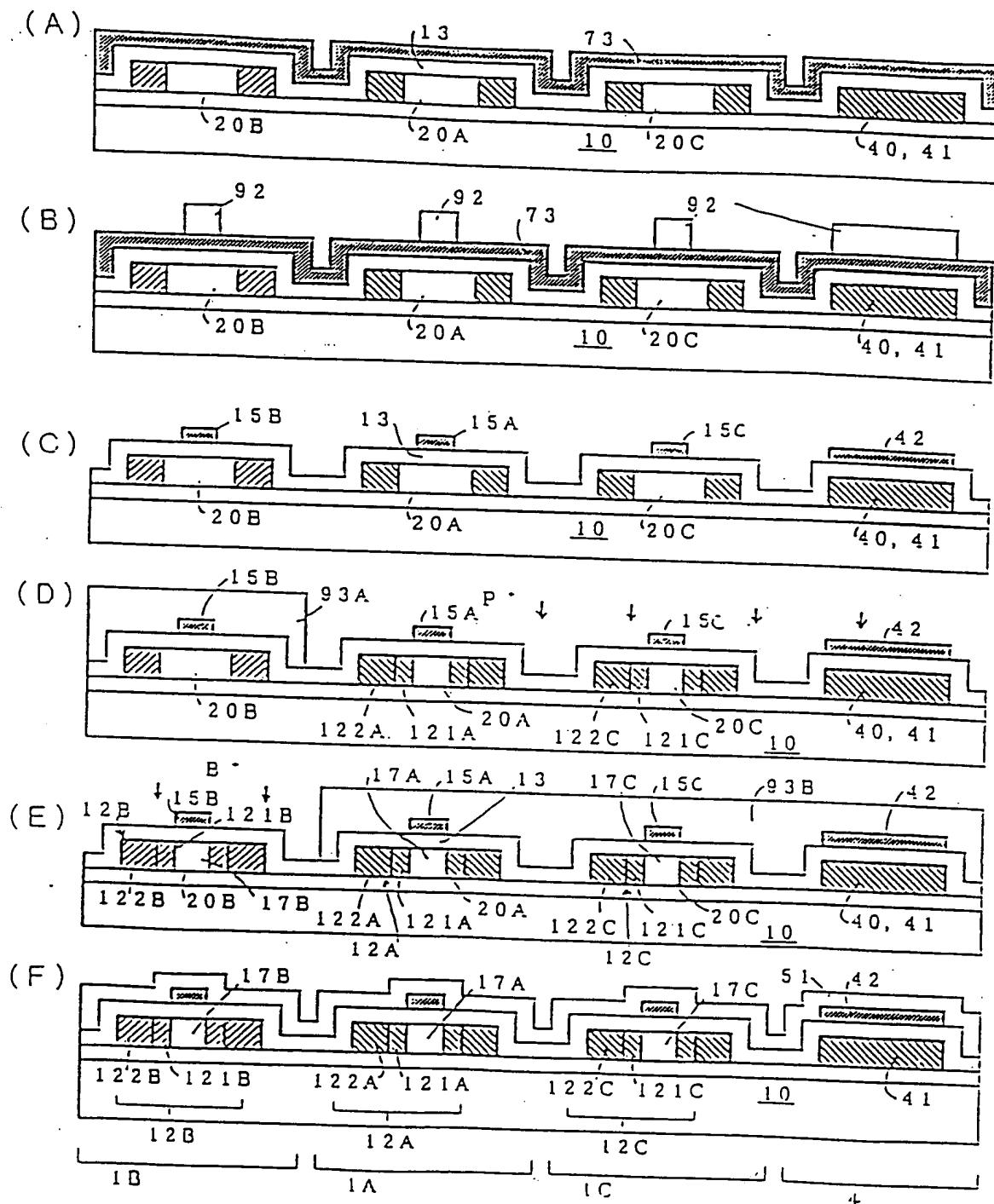


FIG. 13

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P-TYPE TFT FOR
DRIVING CIRCUIT

N-TYPE TFT FOR
DRIVING CIRCUIT

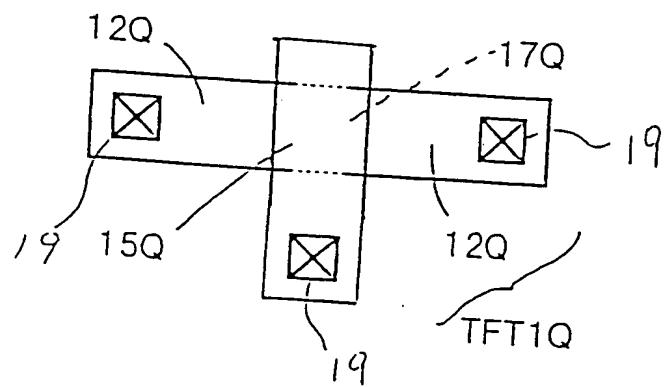
TFT FOR PIXEL

HOLDING CAPACITOR

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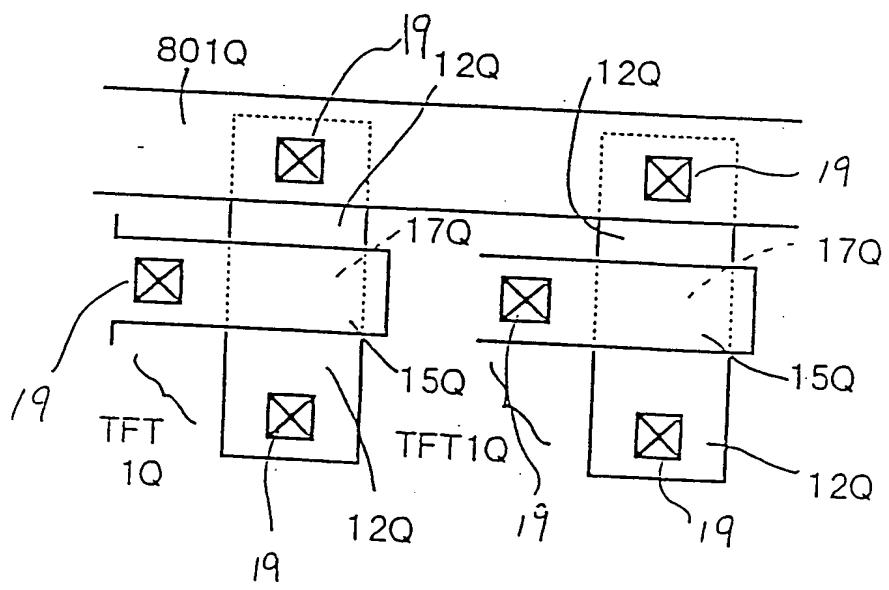
FIG. 14



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FIG. 15



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FIG. 16

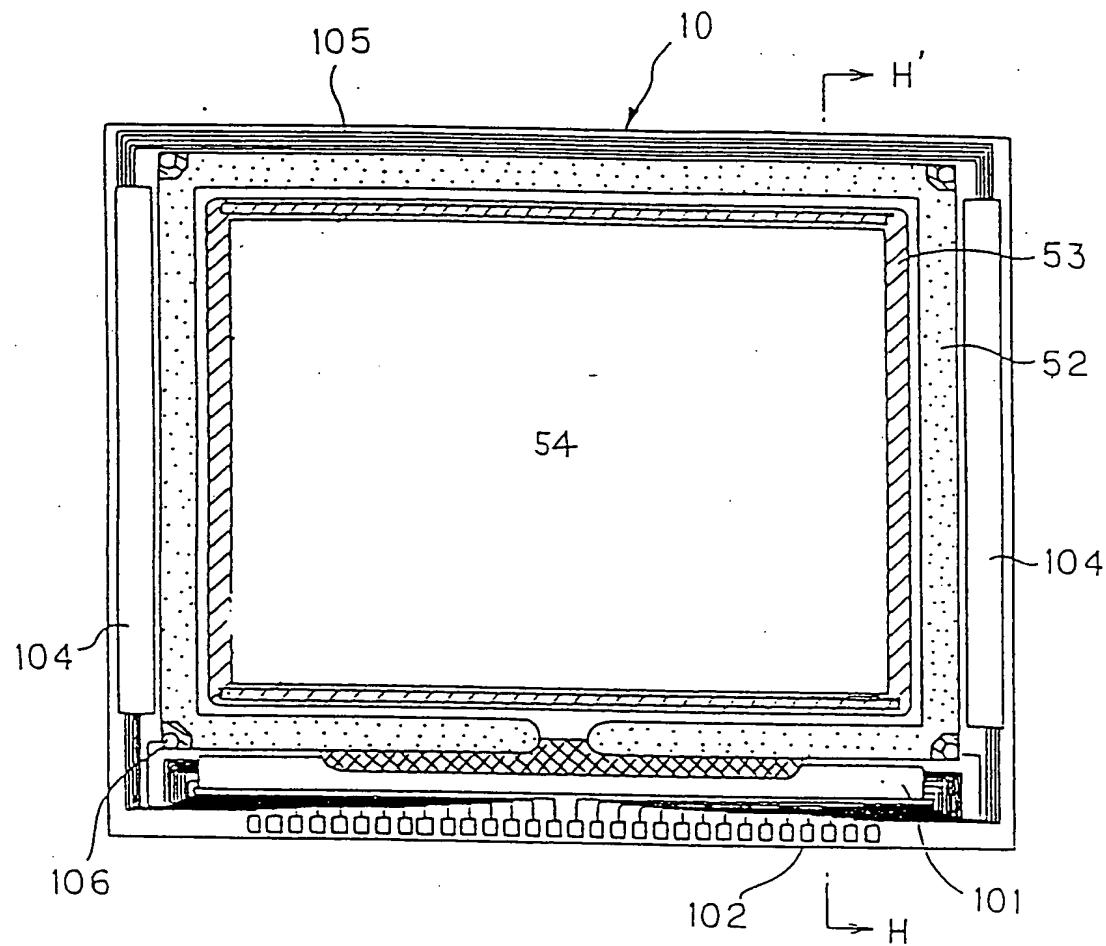
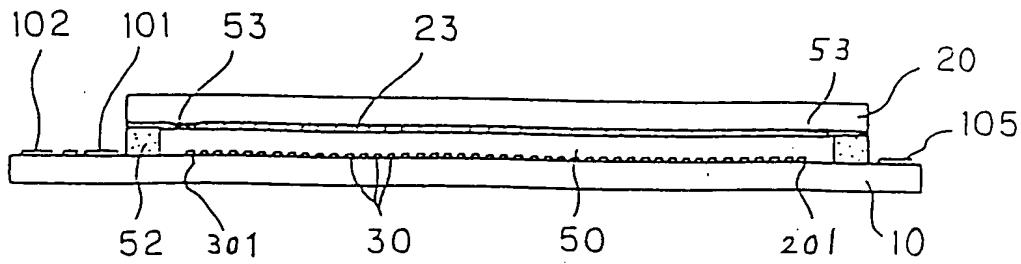
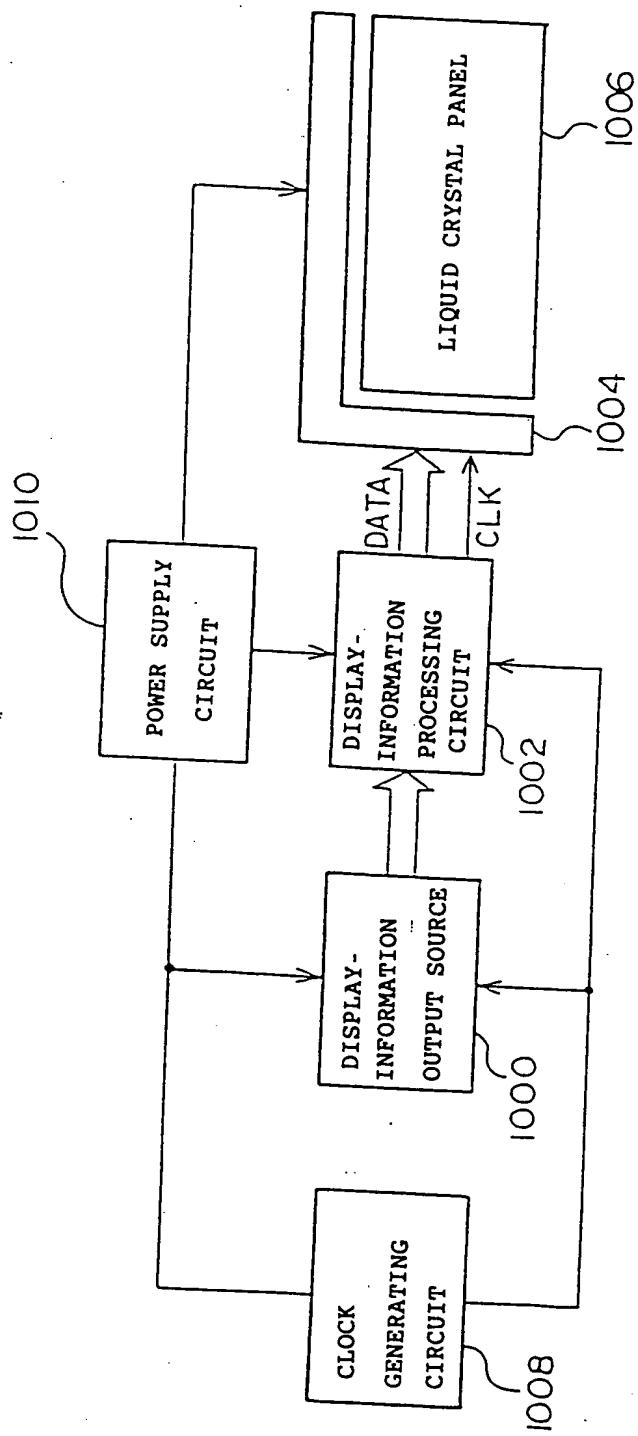


FIG. 17



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FIG. 18



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FIG. 19

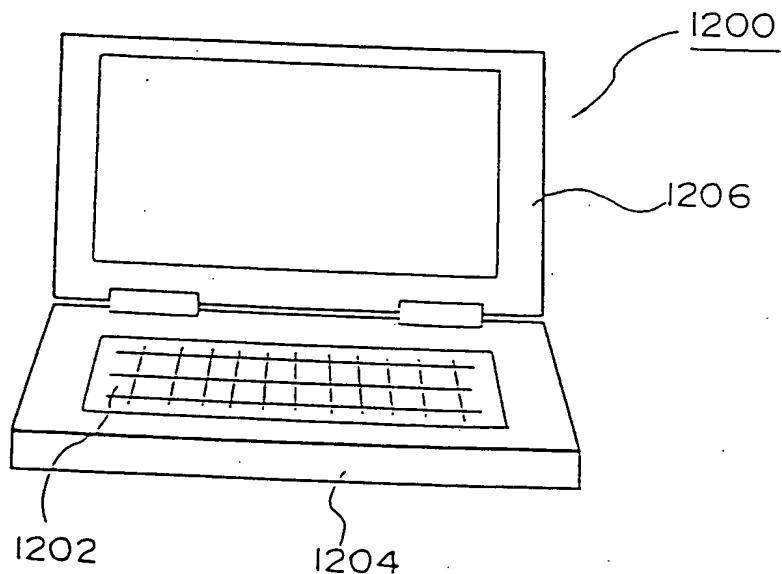
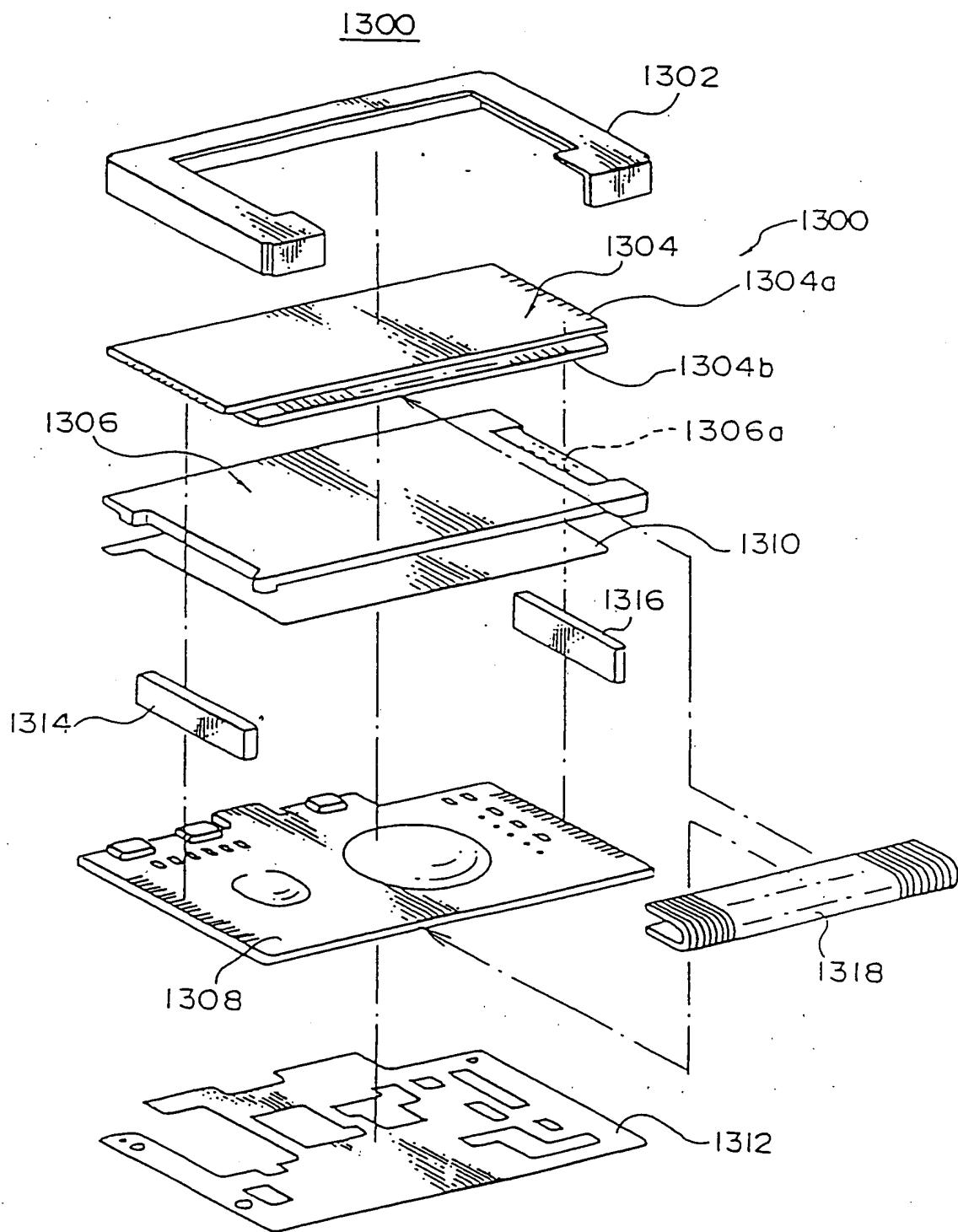


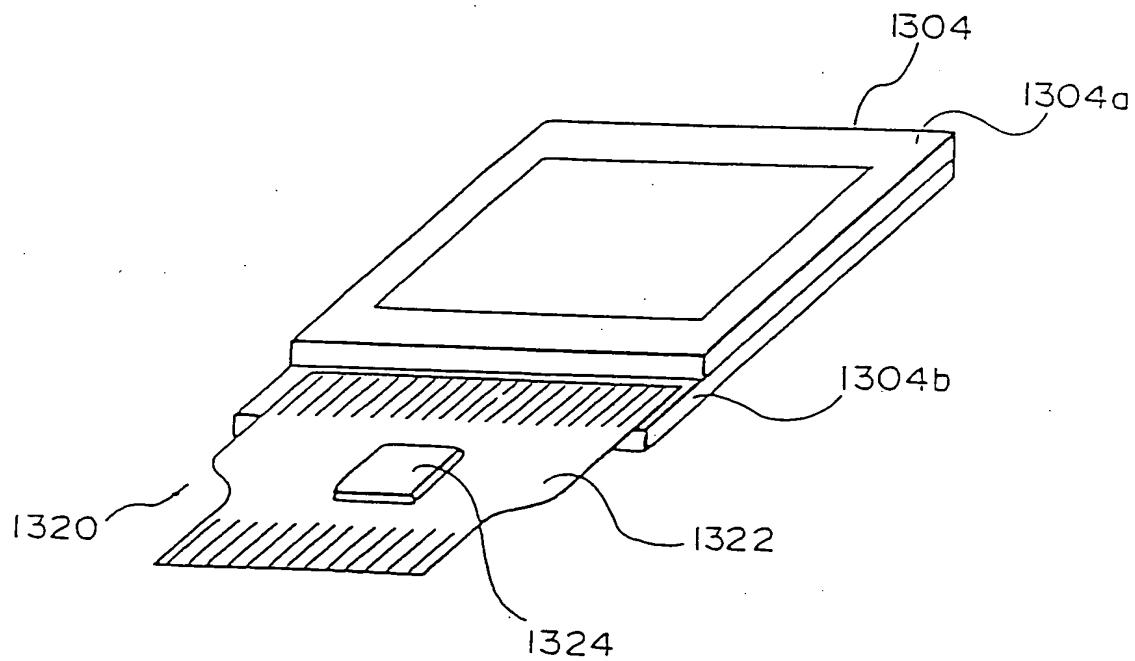
FIG. 20



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FIG. 21



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FIG. 22

